

246841US-2 DIV

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF: :
Junichiro NODA, et al. : EXAMINER:
SERIAL NO. New Application :
FILED: Herewith : GROUP ART UNIT:
FOR: SEMICONDUCTOR MEMORY
DEVICE INCLUDING PAGE
LATCH CIRCUIT

PRELIMINARY AMENDMENT

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

Prior to examination on the merits, please amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks/Arguments begin on page 5 of this paper.